

NOV 01 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Pentakota

Docket No: TI-37302

Serial No: 10/775,022

Examiner: Wells, Kenneth

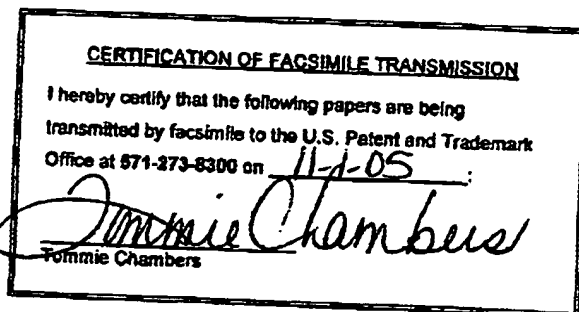
Filed: 2/9/2004

Art Unit: 2816

For: OUTPUT BUFFER FOR DIGITAL LOCK SIGNAL USING AN ANALOG  
AMPLIFIER, A CONVERTER CIRCUIT, AND A BIAS CIRCUIT

AMENDMENT UNDER 37 C.F.R. § 1.116

Assistant Commissioner For Patents  
Washington, DC 20231



OK to enter  
Dear Sir:

11/1/05  
Responsive to the Office Action mailed August 5, 2005 in connection with the  
above identified application, Applicants respectfully submit the following amendments  
and remarks.

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